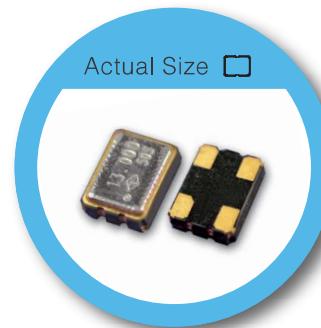


OX Type 3.2 x 2.5 mm SMD Crystal Oscillator

FEATURE

- Typical 3.2 x 2.5 x 0.95 mm ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Operation voltage: 1.8V, 2.5V, 3.3V
- Tri-state enable/disable



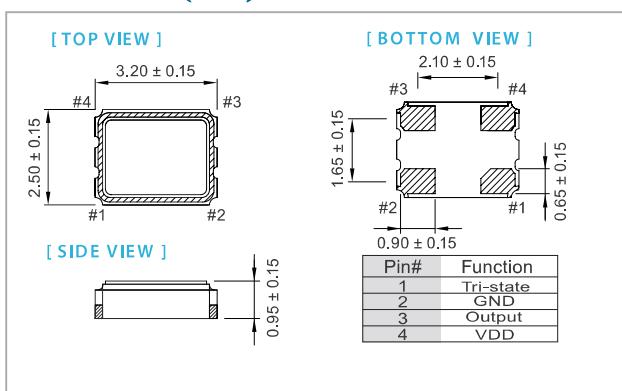
Actual Size □

RoHS Compliant

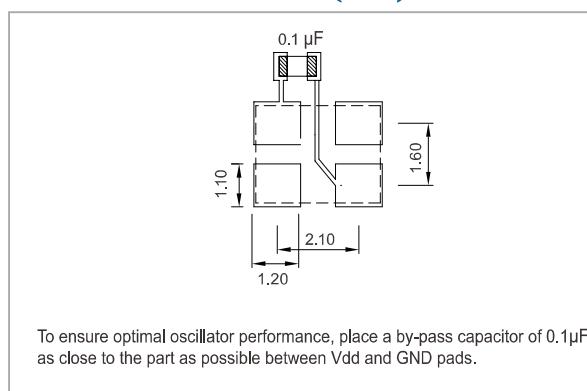
TYPICAL APPLICATION

- WLAN/WiMAX
- Mobile Phone
- DSC, Set-top Box, HDTV

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



ELECTRICAL SPECIFICATION

Parameter	3.3 V		2.5 V		1.8 V		unit
	Min.	Max.	Min.	Max.	Min.	Max.	
Supply Voltage Variation (VDD) ±10%	2.97	3.63	2.25	2.75	1.62	1.98	V
Frequency Range	1.25	125	1.25	125	1.25	125	MHz
Standard Frequency			24, 26, 32, 38.4, 40				
Supply Current	1.25 MHz ≤ Fo < 100 MHz 100 MHz ≤ Fo ≤ 125 MHz	— —	15 25	— —	10 20	— —	mA
Duty Cycle	45	55	45	55	45	55	%
Output Level (CMOS)	Output High (Logic "1") Output Low (Logic "0")	2.97 —	— 0.33	2.25 —	— 0.25	1.62 —	V
Transition Time: Rise/Fall Time⁺	1.25 MHz ≤ Fo < 20 MHz 20 MHz ≤ Fo ≤ 125 MHz	— —	3 2	— —	4 3	— —	nSec
Start Time	—	2	—	2	—	2	mSec
Tri-State(Input to Pin 1)	Enable (High voltage or floating) Disable (Low voltage or GND)	2.31 —	— 0.99	1.75 —	— 0.75	1.26 —	V
Period Jitter(Pk-Pk)	—	40	—	40	—	40	pSec
RMS Phase Jitter (Integrated 12 kHz ~ 20 MHz)	—	1	—	1	—	1	pSec
Standby Current	—	10	—	10	—	10	μA
Aging (@ 25°C 1st year)	—	±3	—	±3	—	±3	ppm
Storage Temp. Range	-55	125	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

+ Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	ppm	±20	±25	±50
-10 ~ +60	○	○	○	
-20 ~ +70	△	○	○	
-40 ~ +85	X	○	○	

* ○: Available △:Conditional X: Not available

* Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration

Note: not all combination of options are available. Other specifications may be available upon request.